

Figure 1

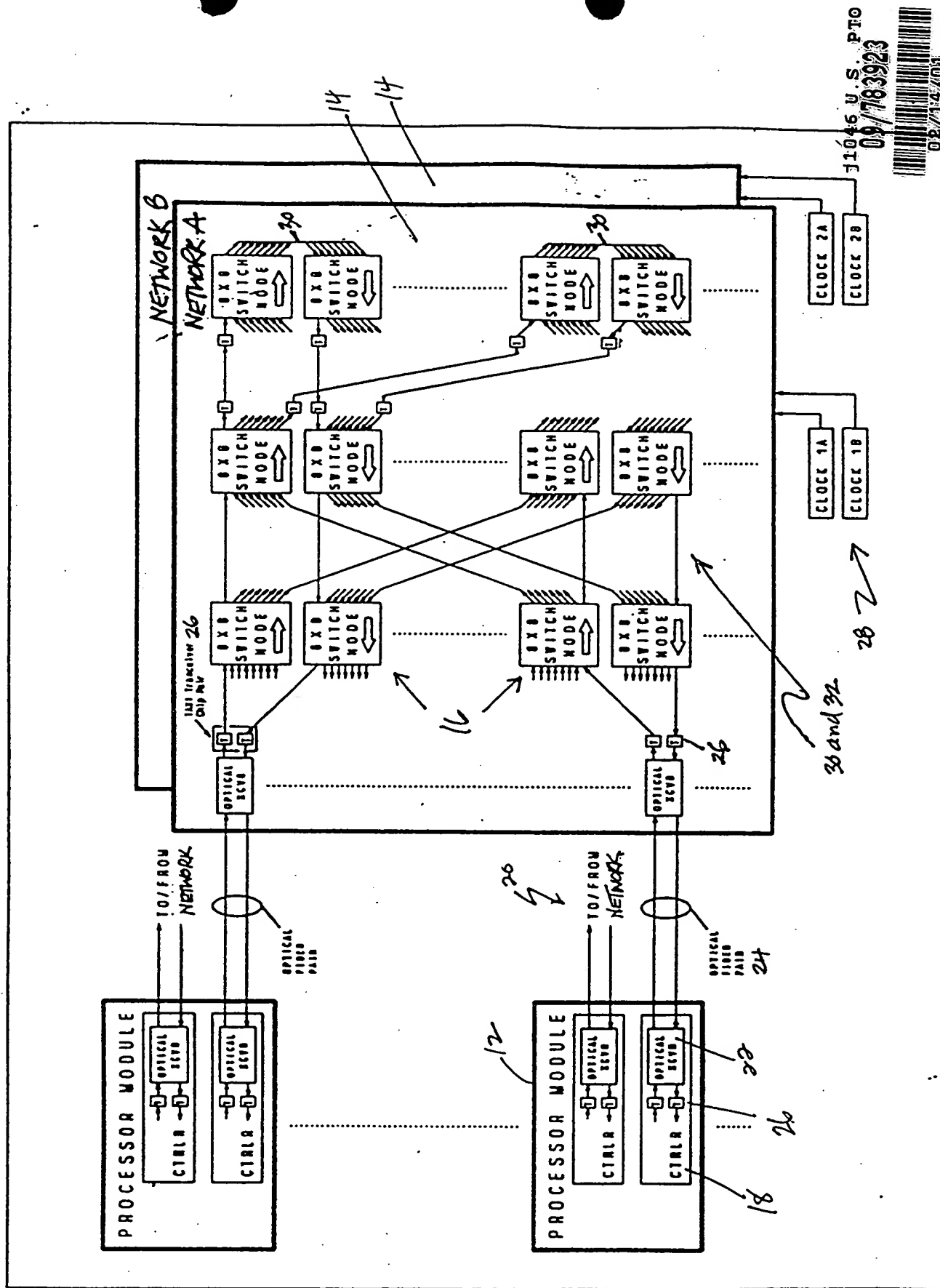


Figure 2

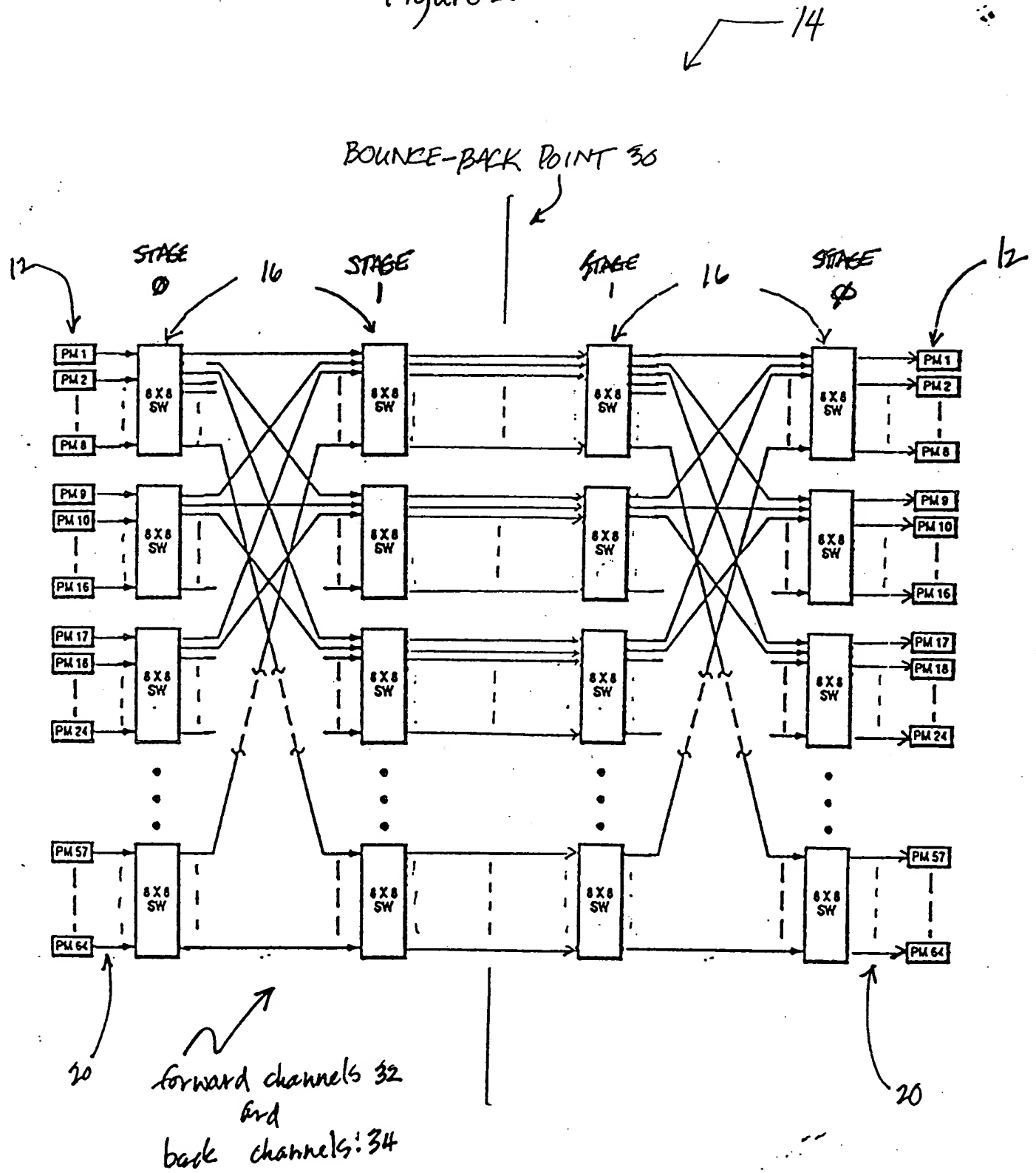


Figure 3

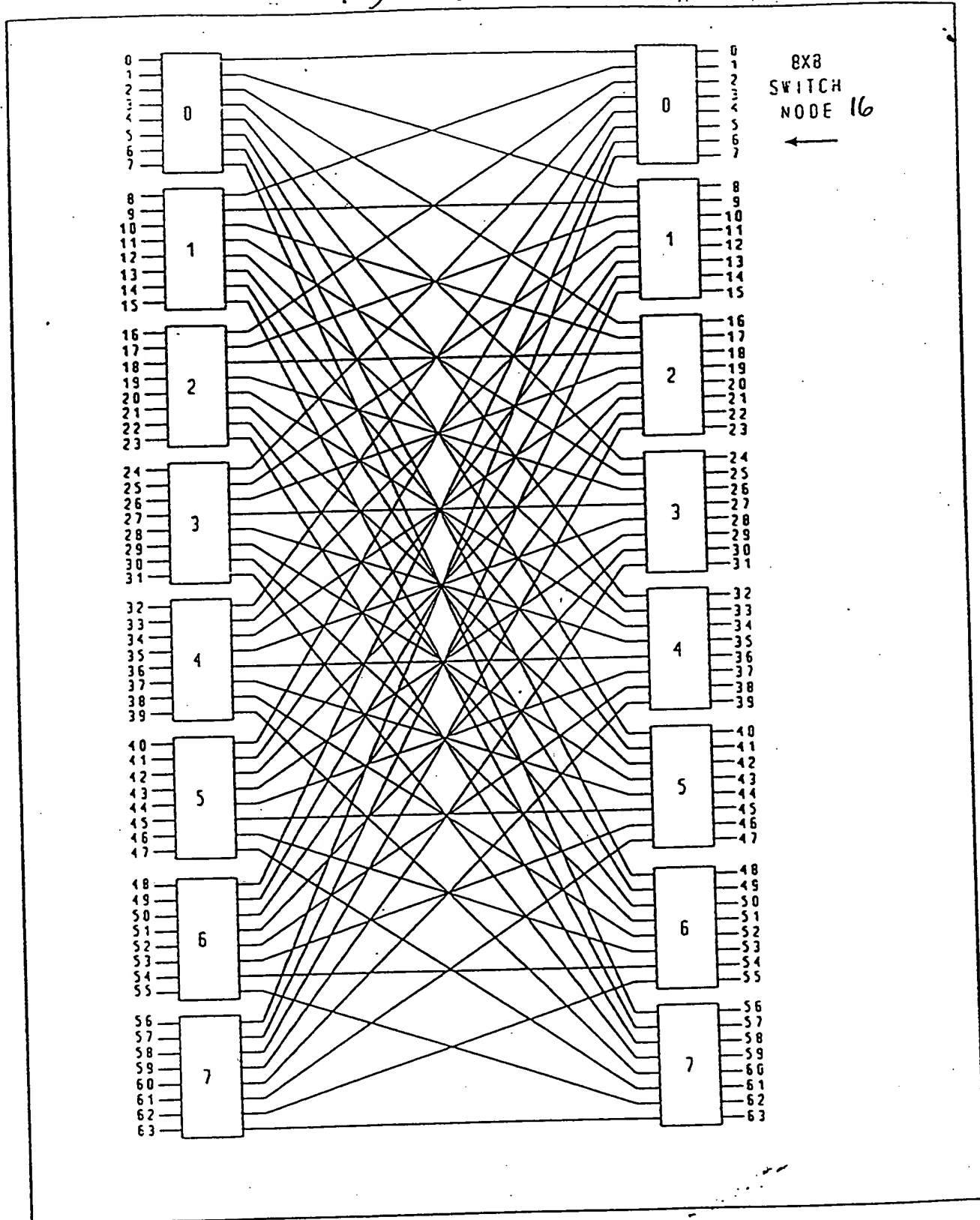


Figure 4

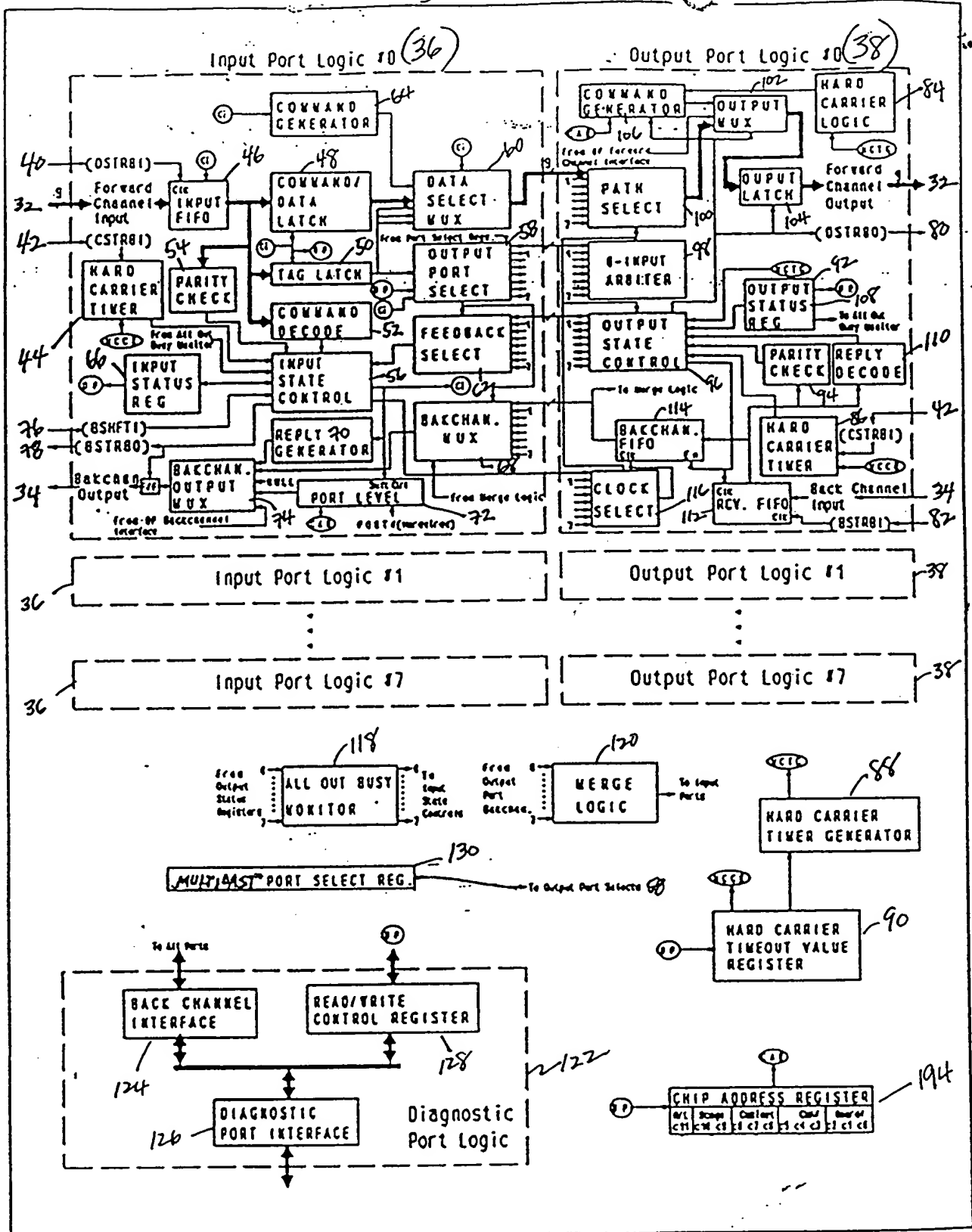


Figure 5

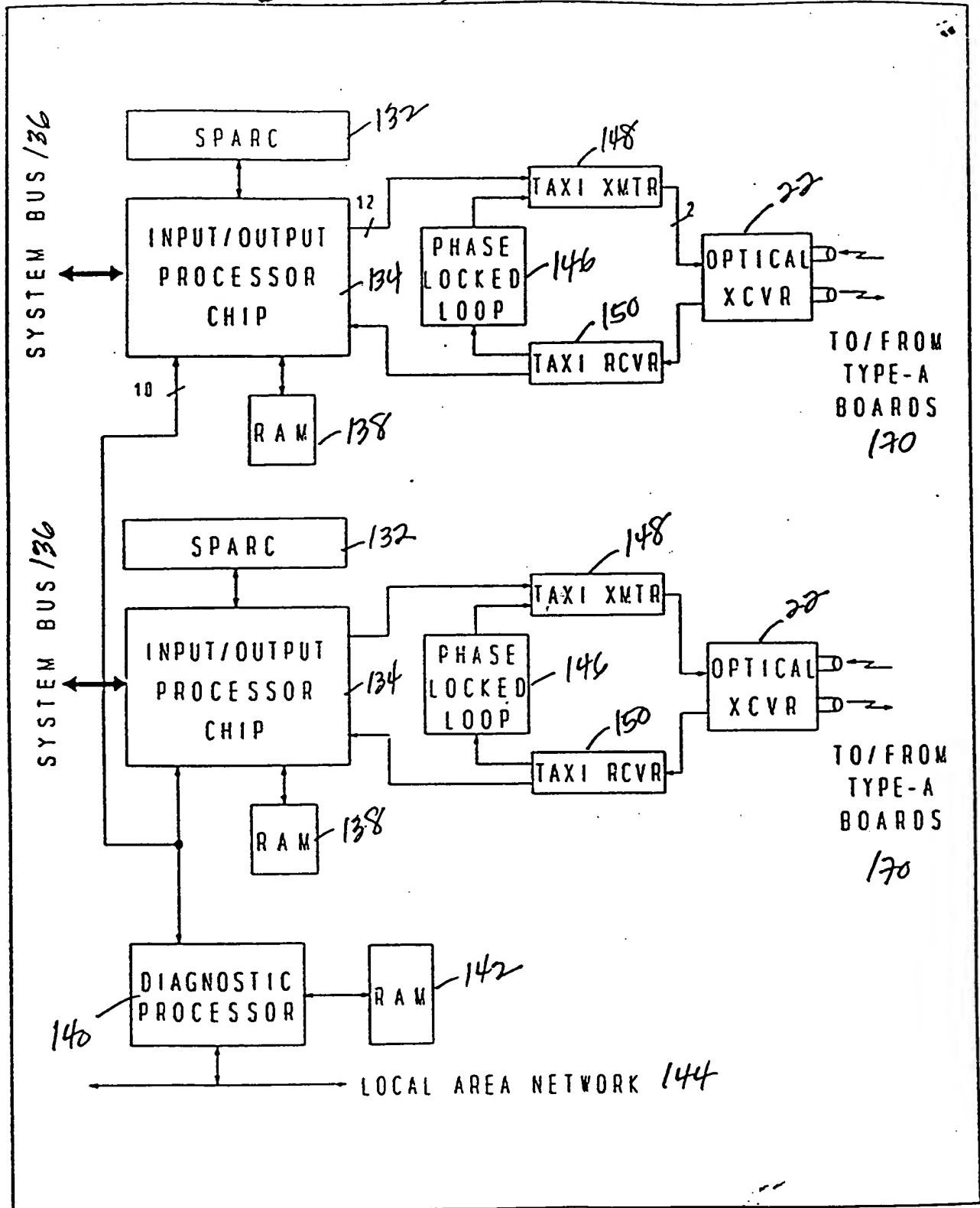


Figure 6

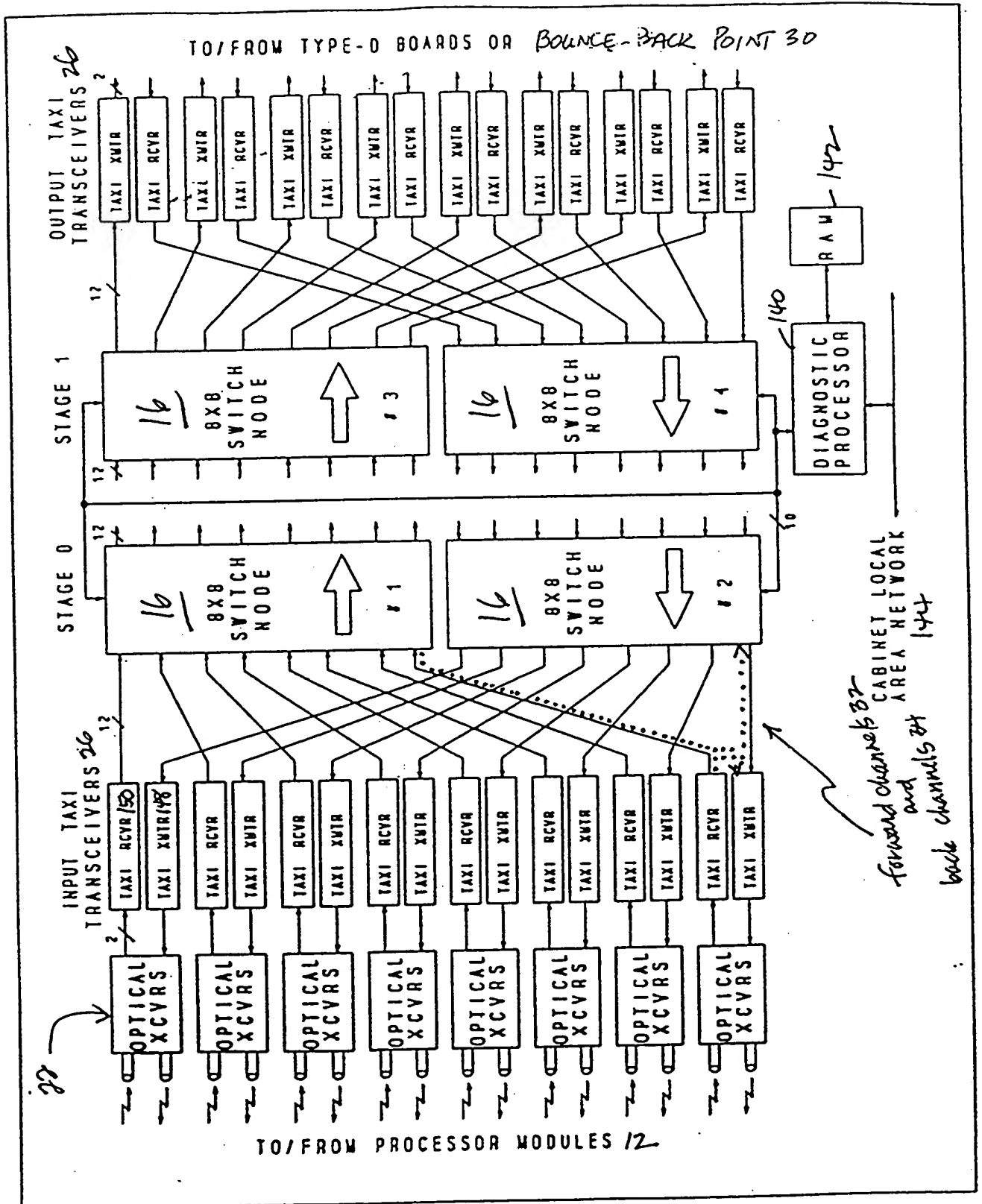




Figure 8 ✓ 174

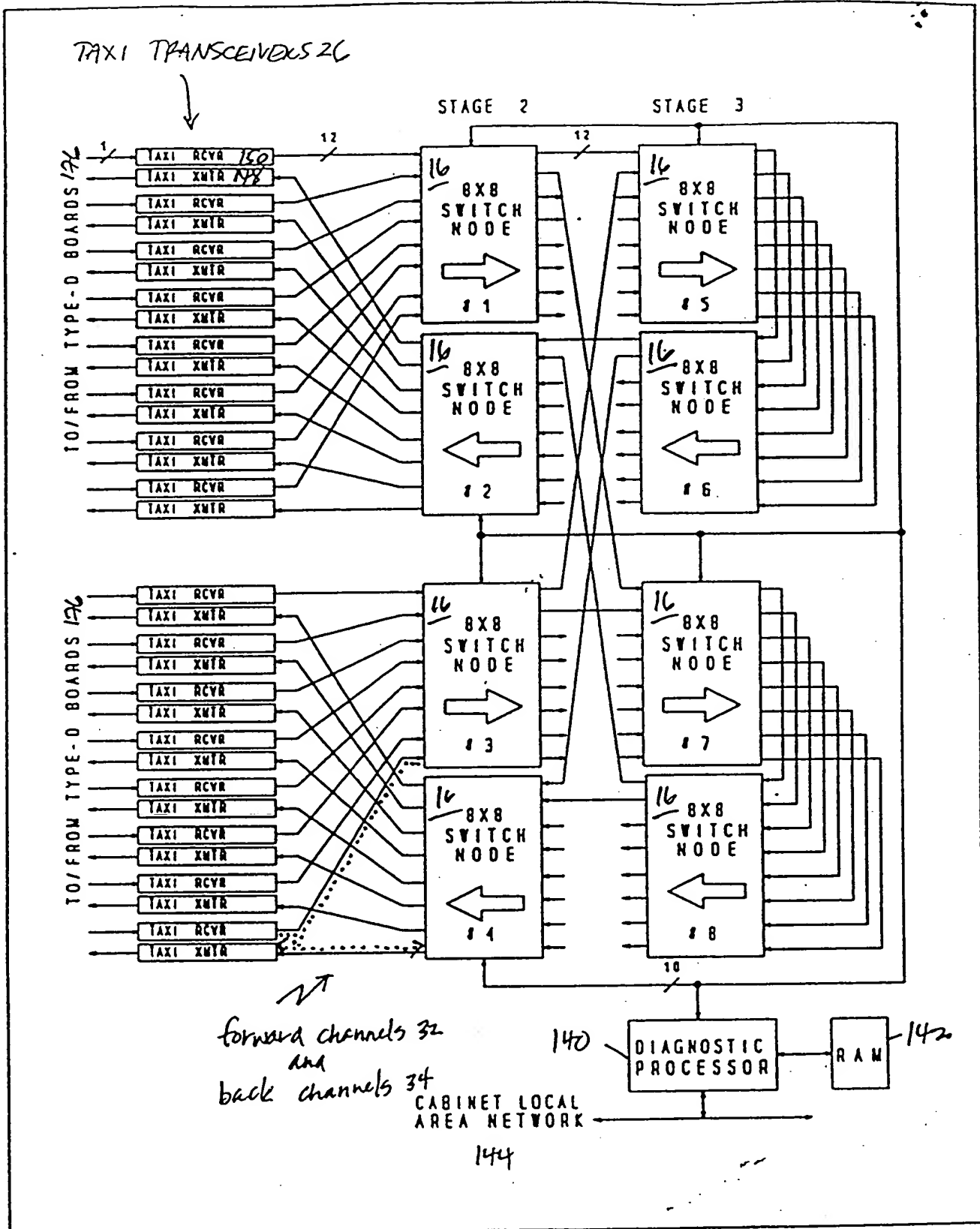




Figure 9  
(≤ 64 PORTS)

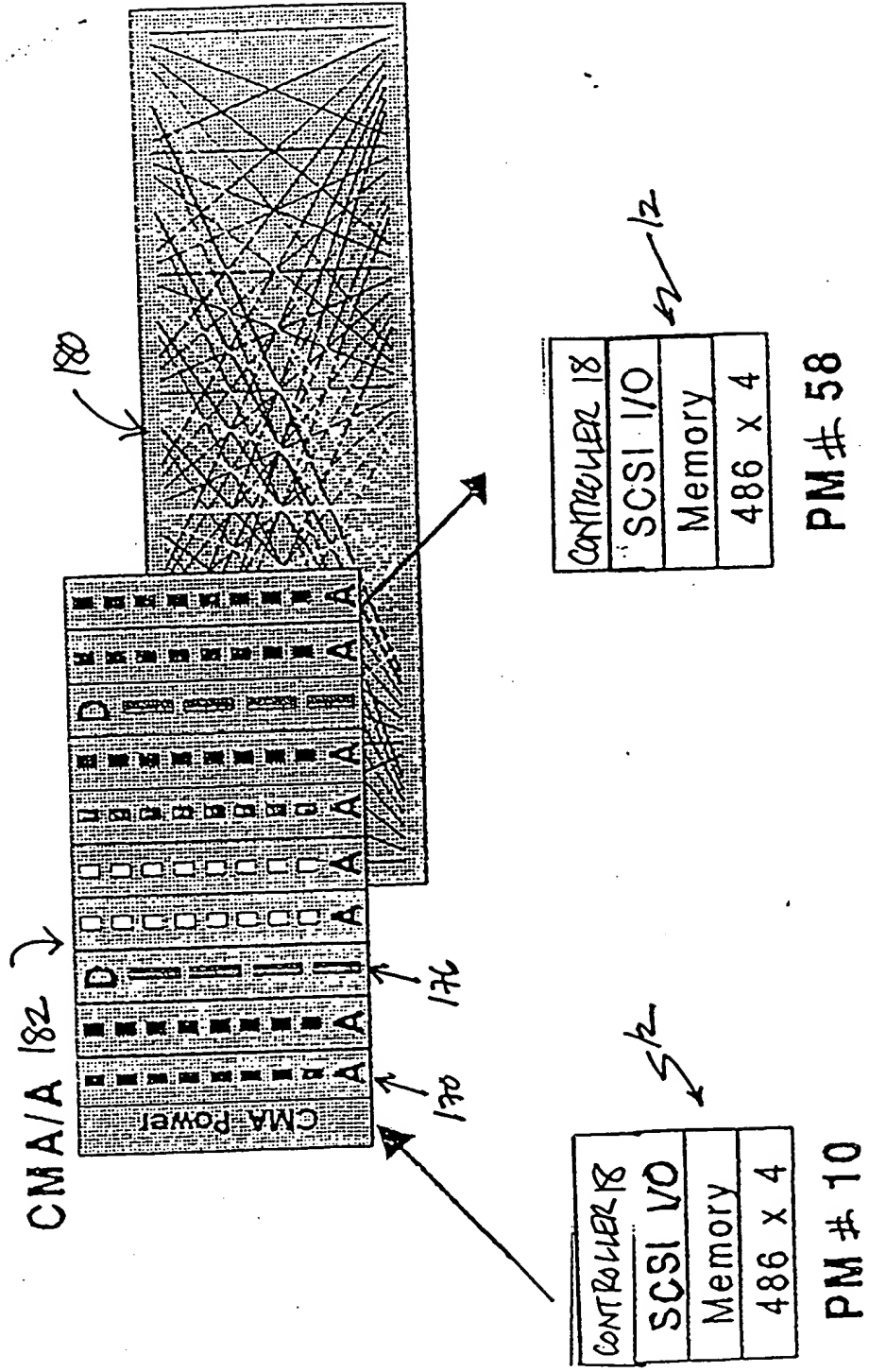


Figure 10

# CMA/A 182 Circuit Switching

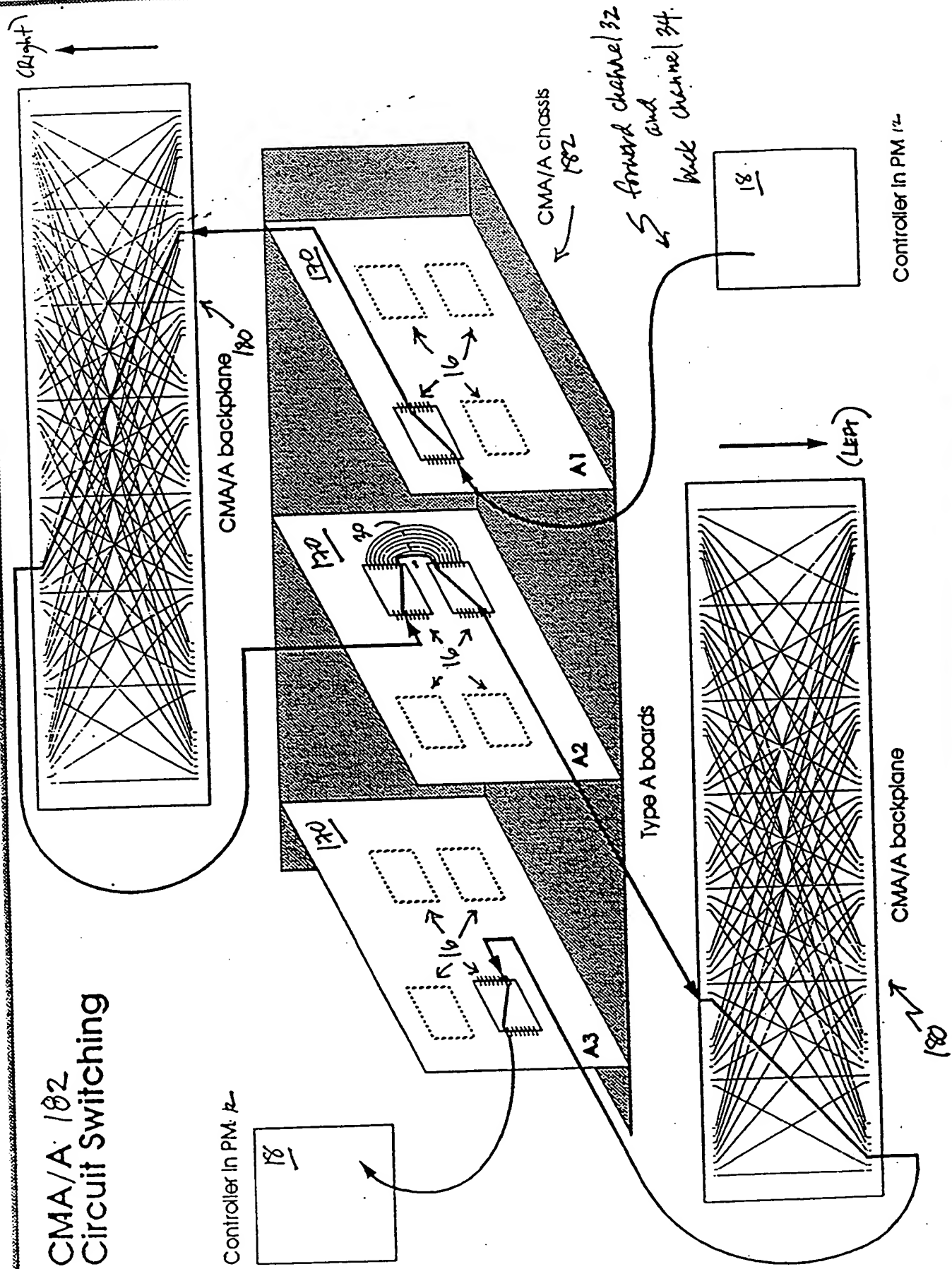
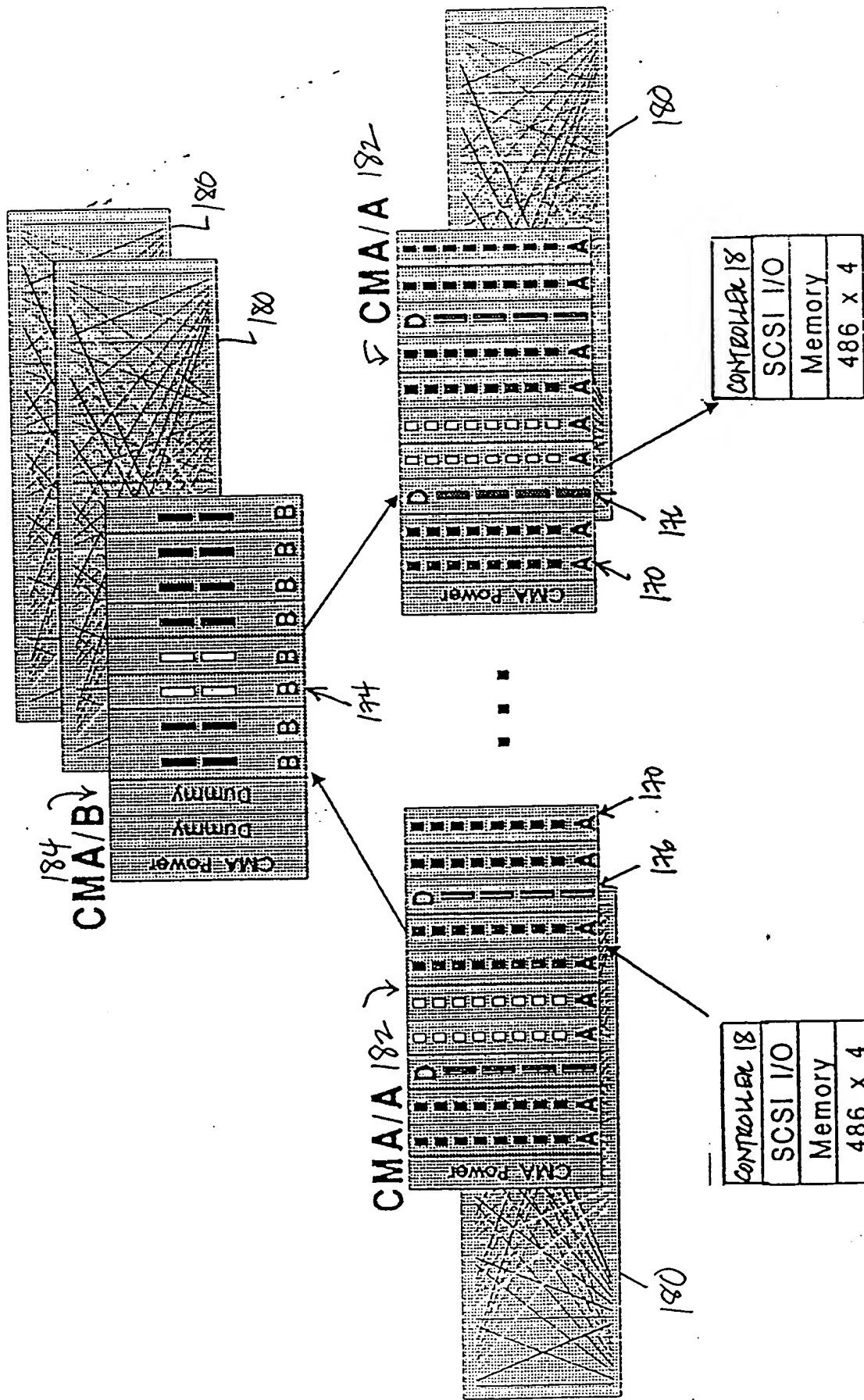


Figure 11  
 $(64 < \text{PORT} \leq 512)$



PM # 375

PM # 10



Figure 13

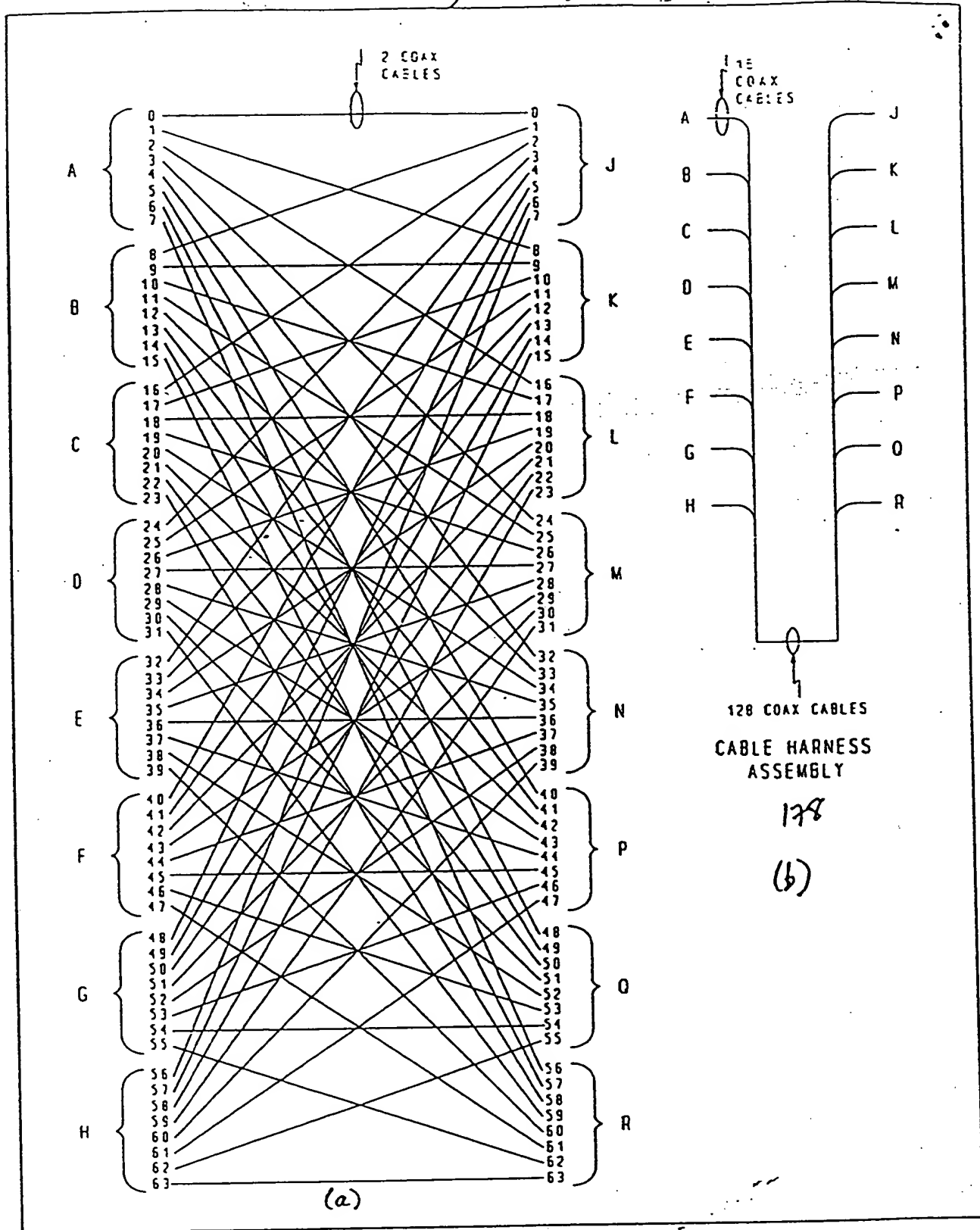


Figure 14

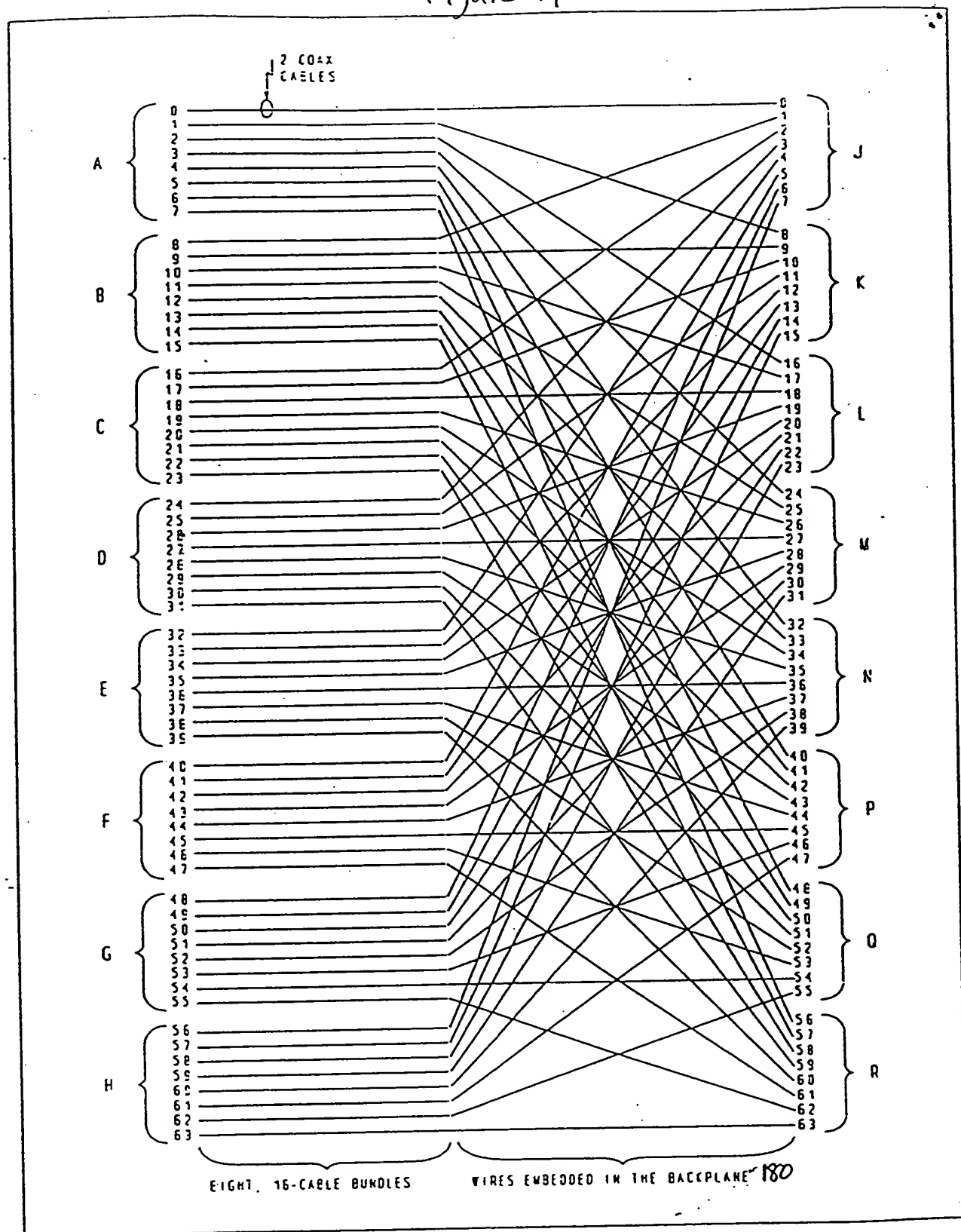


Figure 15

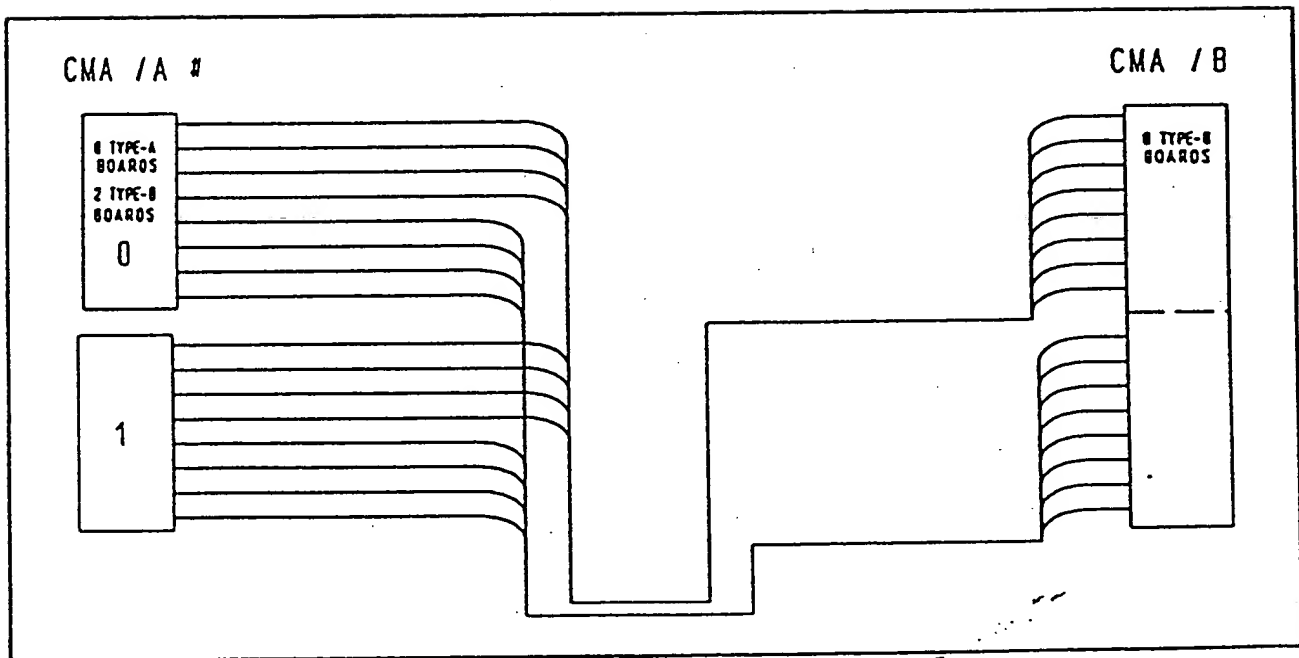
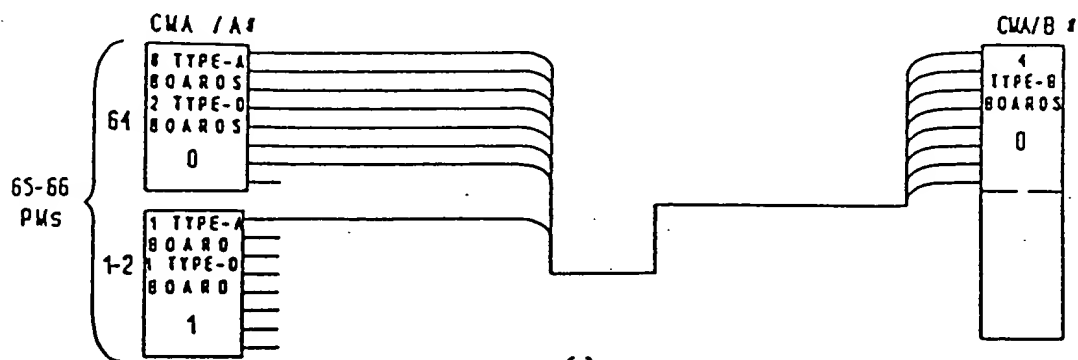
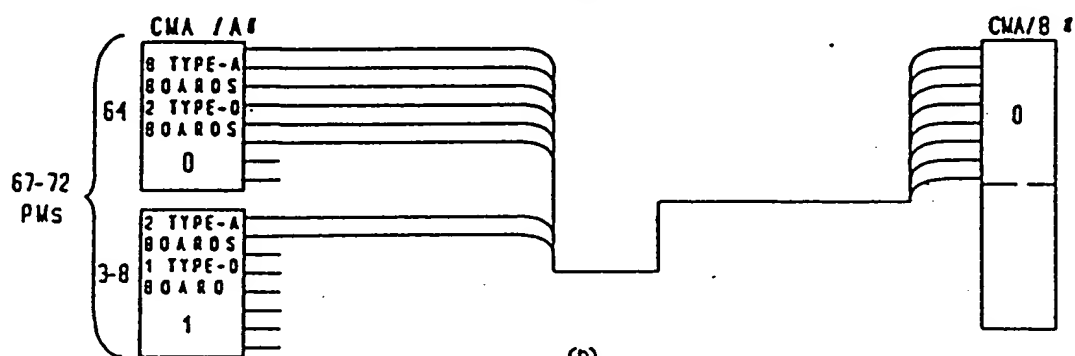


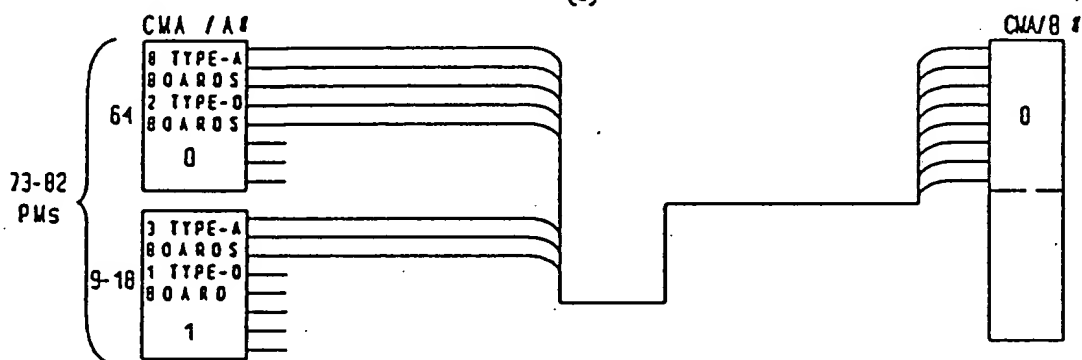
Figure 16



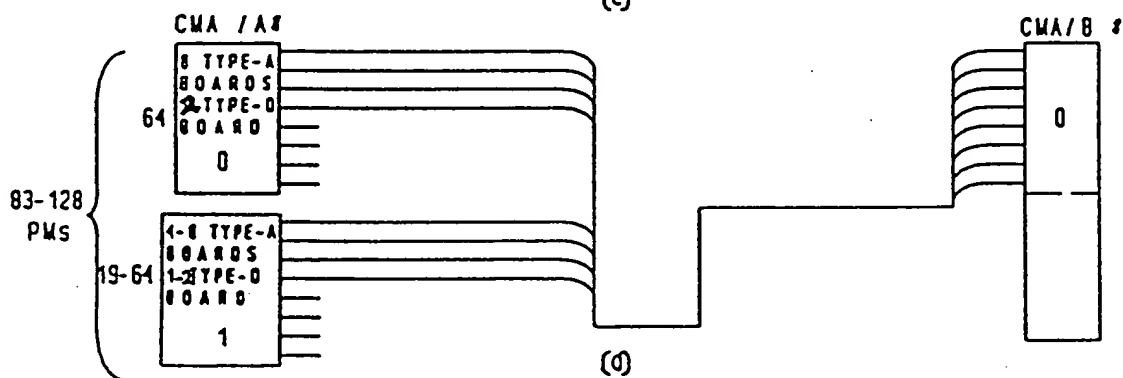
(a)



(b)



(c)



(d)



Figure 17

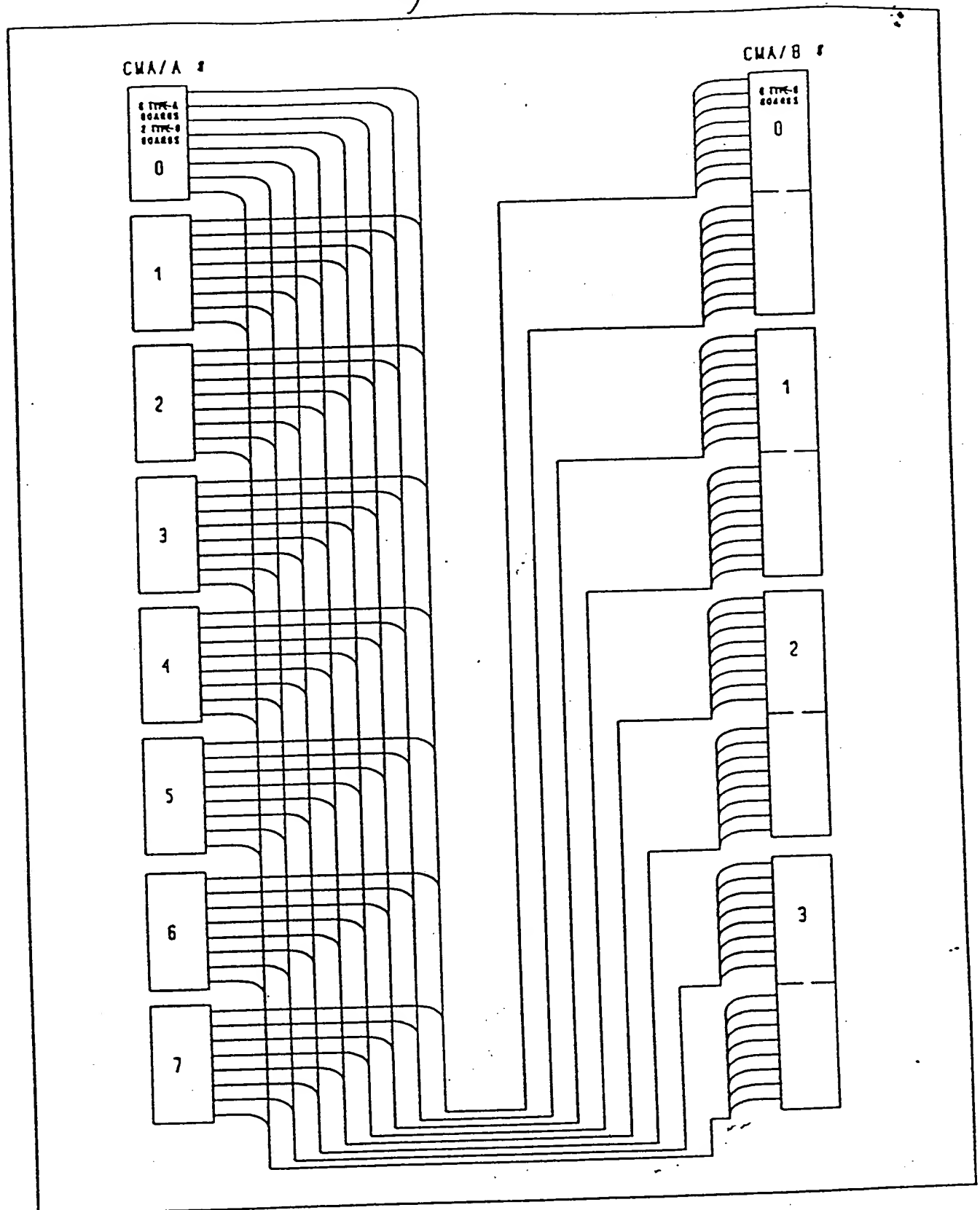


Figure 18

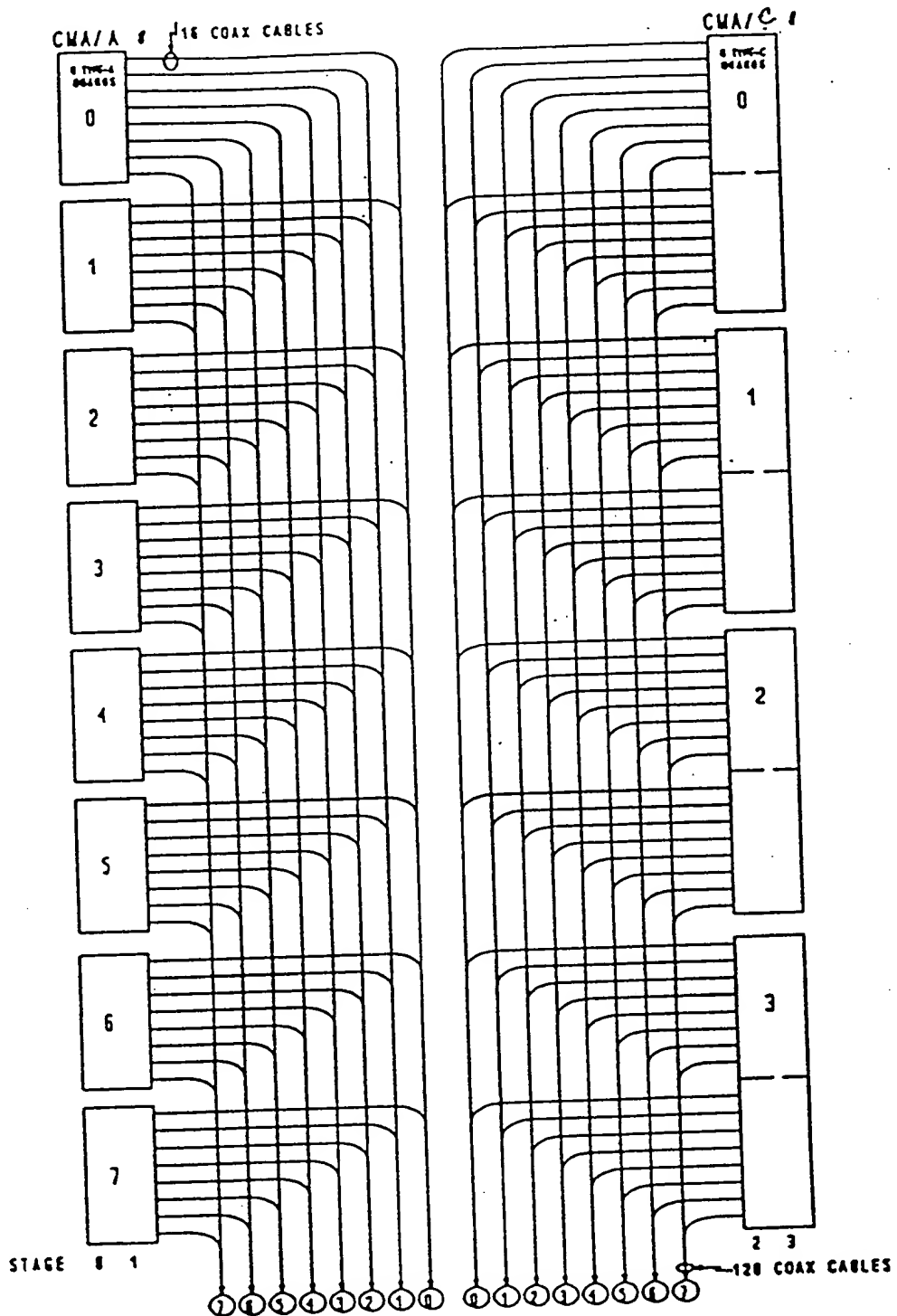


Figure 19

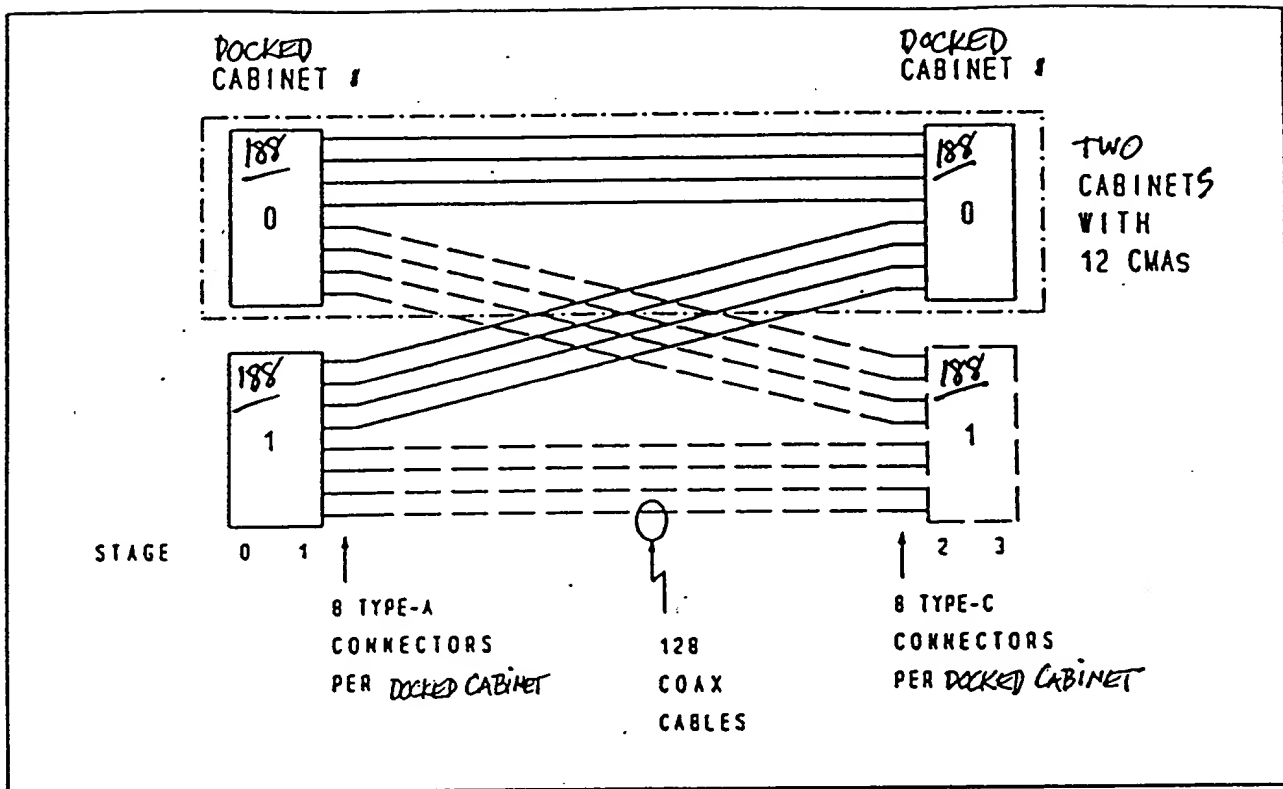


Figure 20

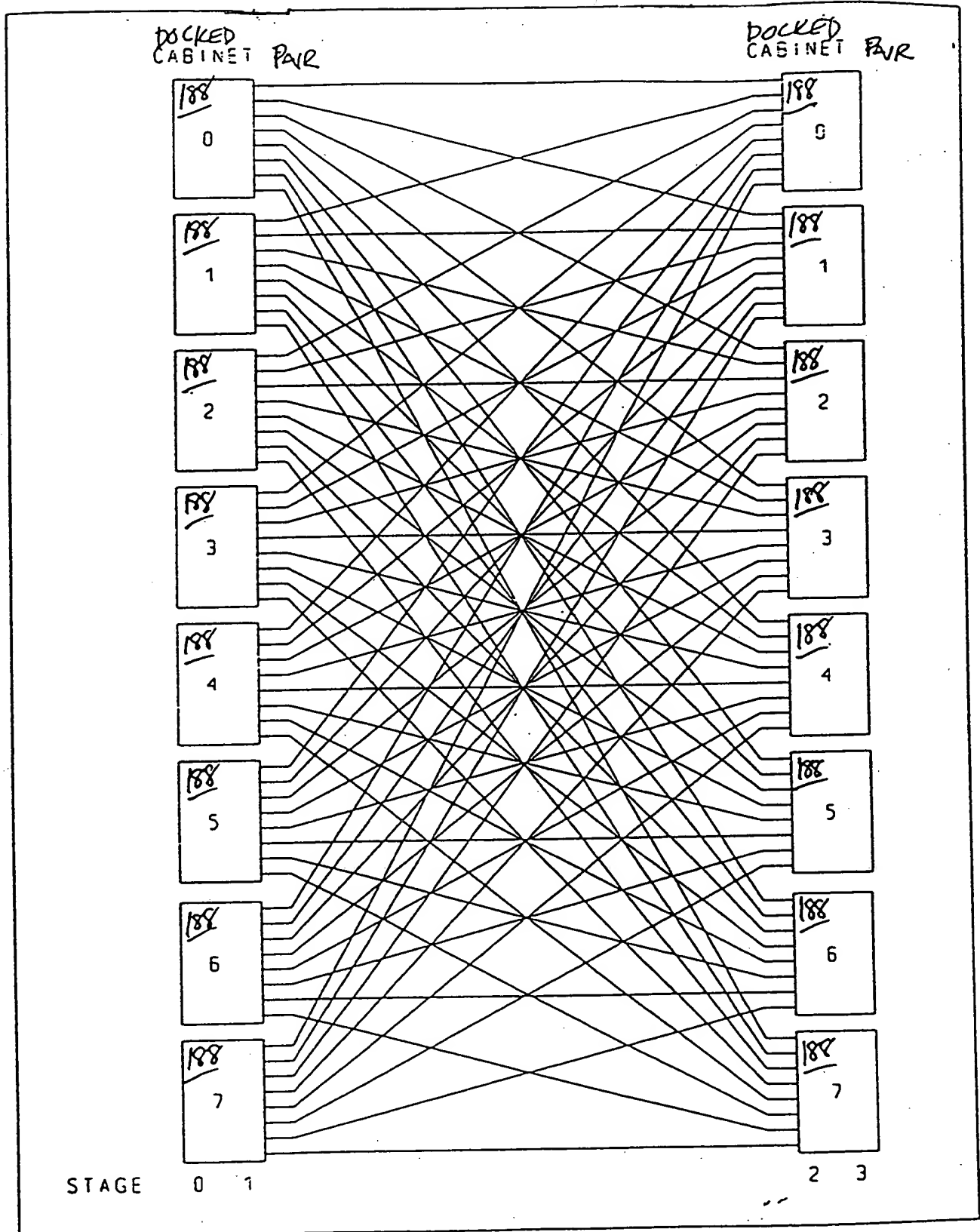


Figure 21

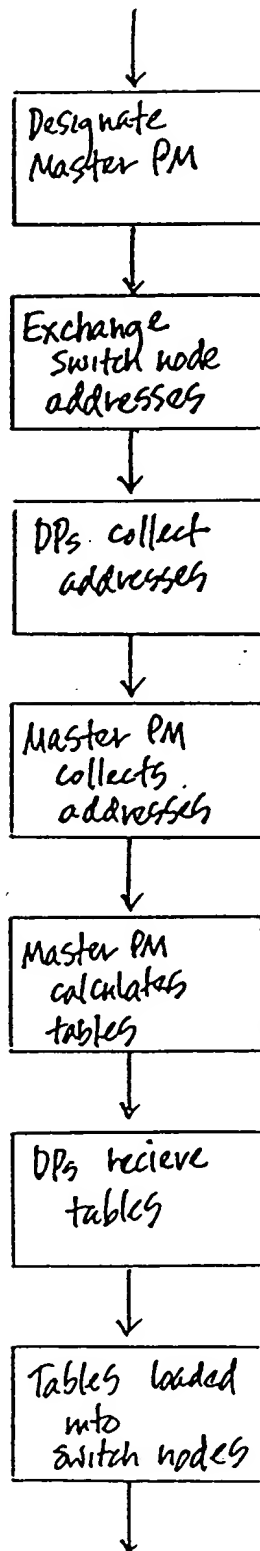


Figure 22

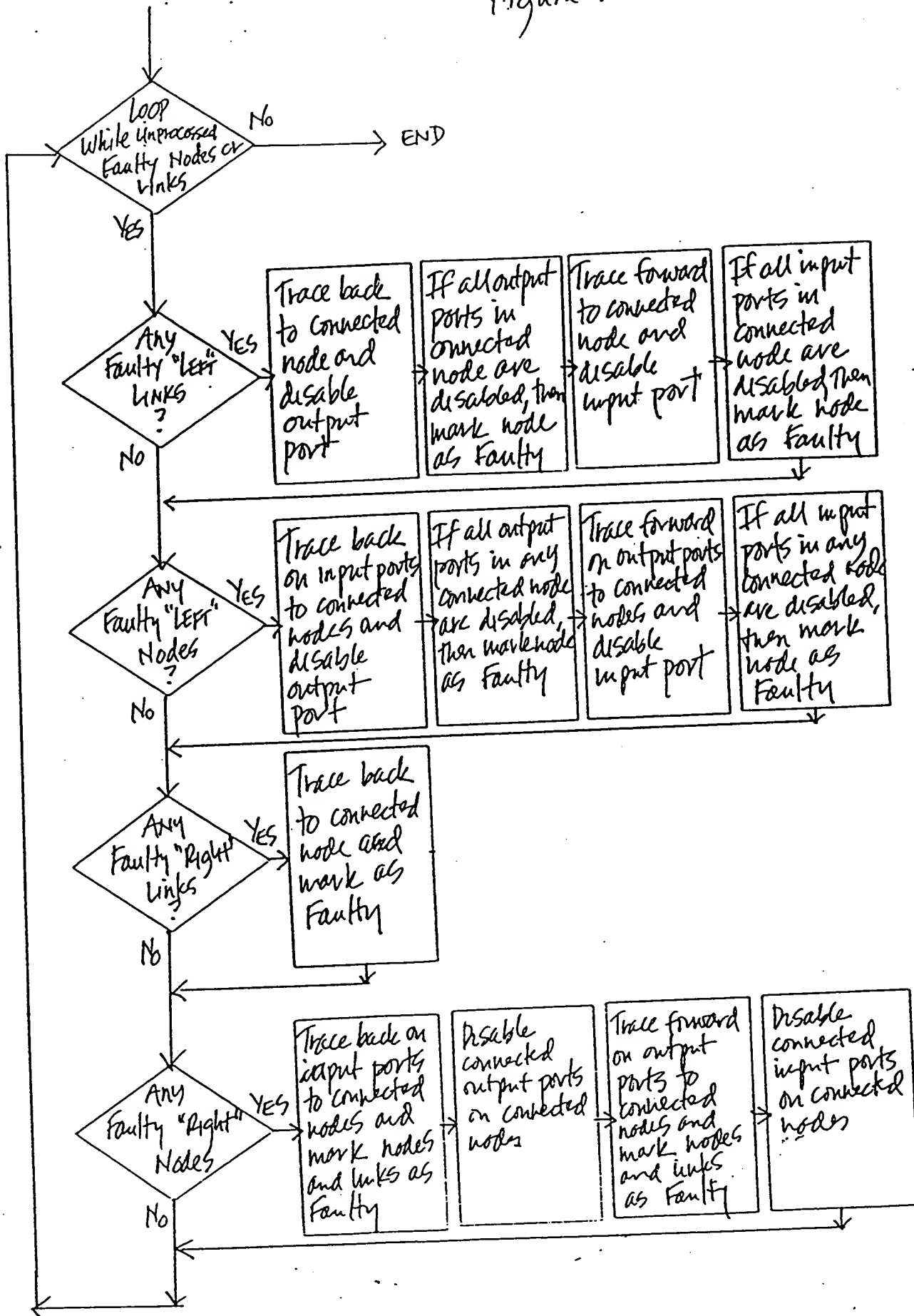
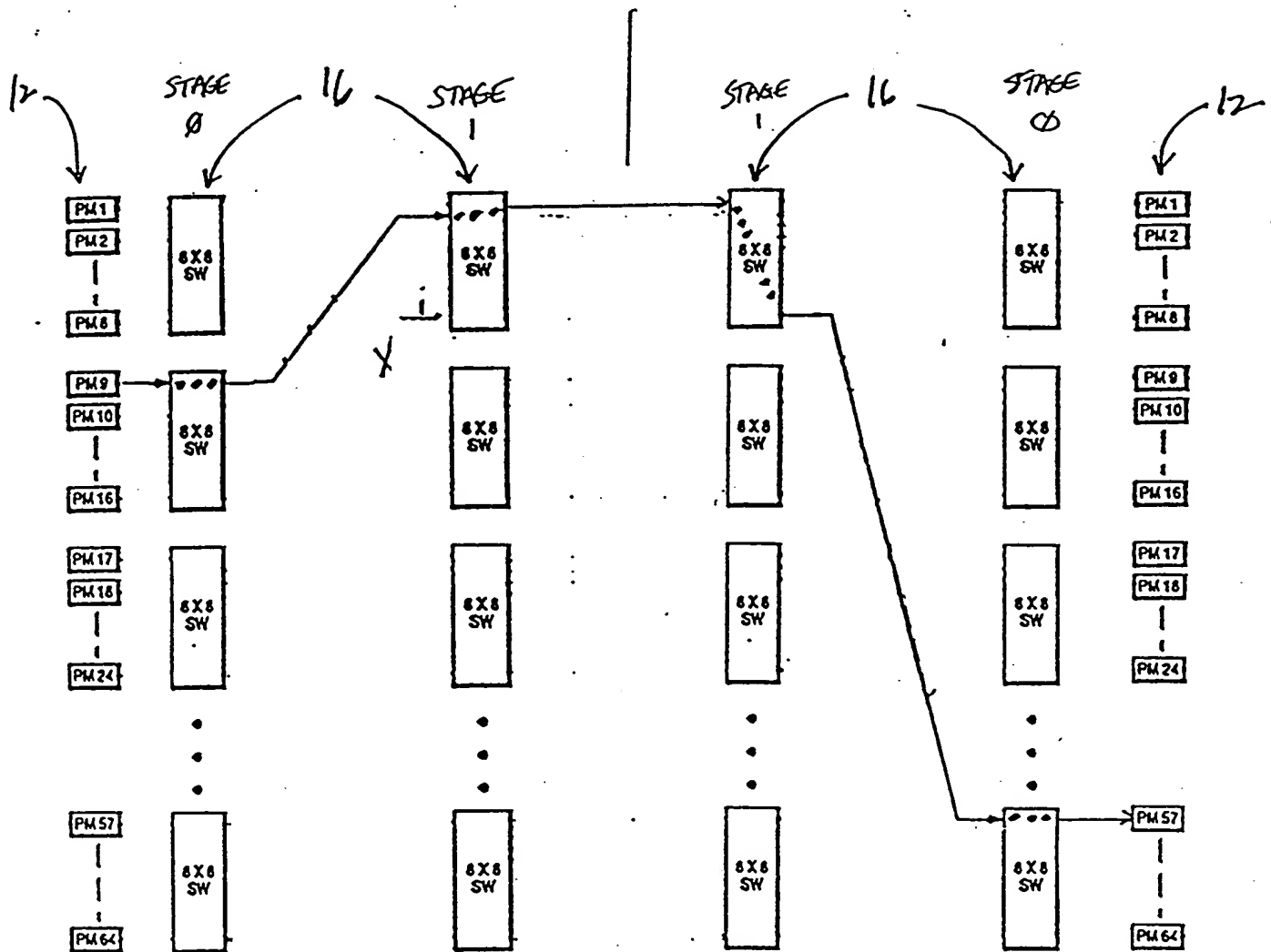


Figure 23

LOOP-BACK POINT 30



The diagram illustrates a network interface system (150) with the following components and connections:

- Request (160):** An input queue for requests from the device.
- Response (162):** An output queue for responses to the device.
- Completed Requests:** A queue for requests that have been successfully processed.
- Blocked (161):** A queue for requests that are currently being processed or are waiting for resources.
- Timer (154):** A component that manages timing for the system.
- TxDISPATCHER (152):** A central dispatcher for transmitting data. It receives requests from the Request queue and sends data to the TxSend queue.
- Unblocked Circuits (154):** A queue for circuits that are ready to be transmitted.
- Transmit Circuits (156):** A queue for circuits that are currently being transmitted.
- Backed-Off Circuits:** A queue for circuits that have been temporarily removed from the transmission queue.
- TxSend (164):** A queue for data being sent to the transmitter hardware.
- Transmitter Hardware:** The physical hardware used to transmit data over the network.
- Completed Circuits (166):** A queue for circuits that have been successfully transmitted.
- Device Interface (158):** A component that manages the interface between the device and the network.
- RxDISPATCHER (158):** A central dispatcher for receiving data. It receives data from the Receiver Hardware and sends it to the RxDispatch queue.
- Receive Circuit (158):** A queue for circuits that are currently being received.
- Completed Circuit (162):** A queue for circuits that have been successfully received.
- Unsolicited Input:** An input for data that is received from the network but was not requested by the device.
- Receiver Hardware:** The physical hardware used to receive data from the network.

Handwritten annotations in the diagram include:

- 150:** The overall system identifier.
- 152:** TxDISPATCHER.
- 154:** Timer and Unblocked Circuits.
- 156:** Transmit Circuits.
- 158:** Device Interface and RxDISPATCHER.
- 160:** Request queue.
- 161:** Blocked queue.
- 162:** Response queue.
- 164:** TxSend queue.
- 166:** Completed Circuits queue.

Arrows indicate the flow of data and control signals between these components. A dashed arrow labeled "To Network 14" points from the Transmitter Hardware, and a dashed arrow labeled "From Network 14" points to the Receiver Hardware.

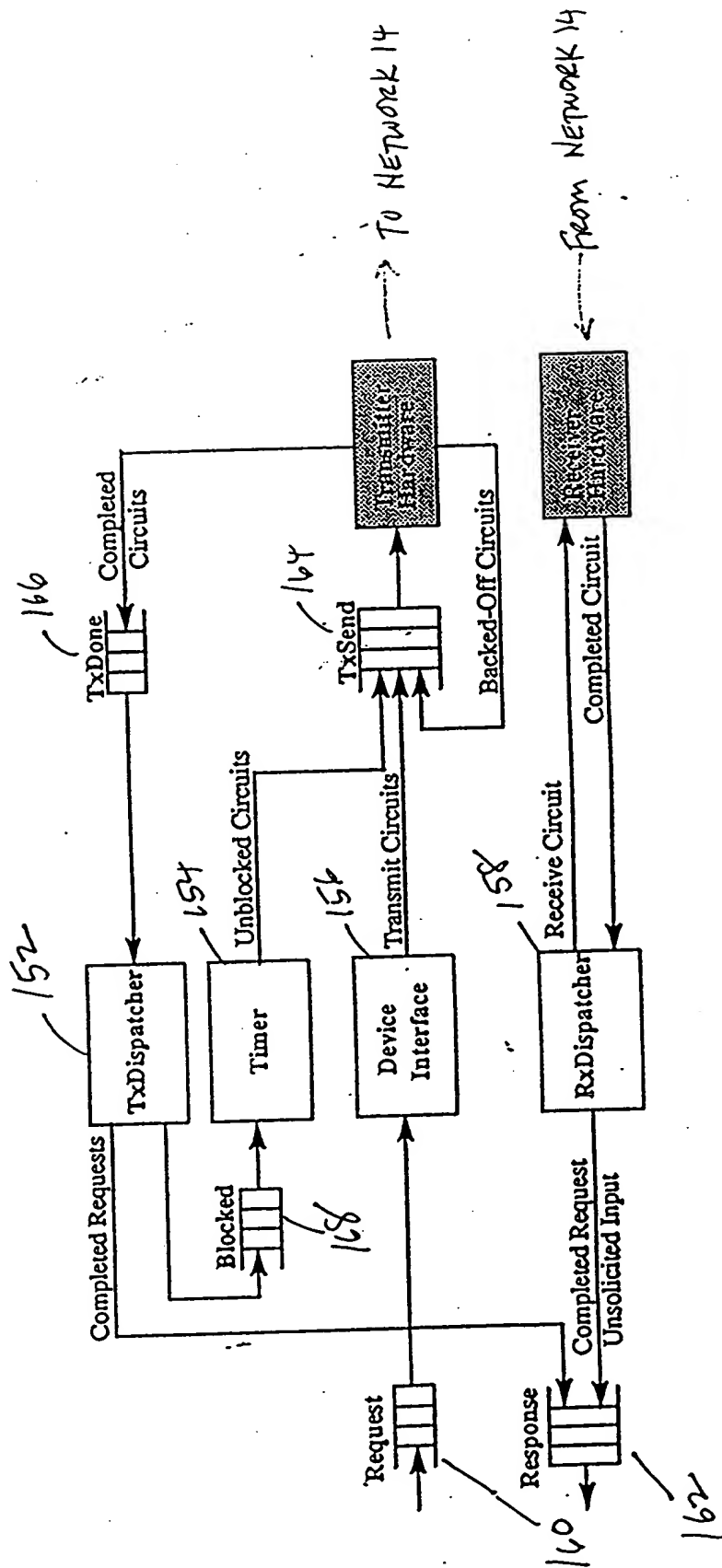




Figure 25.

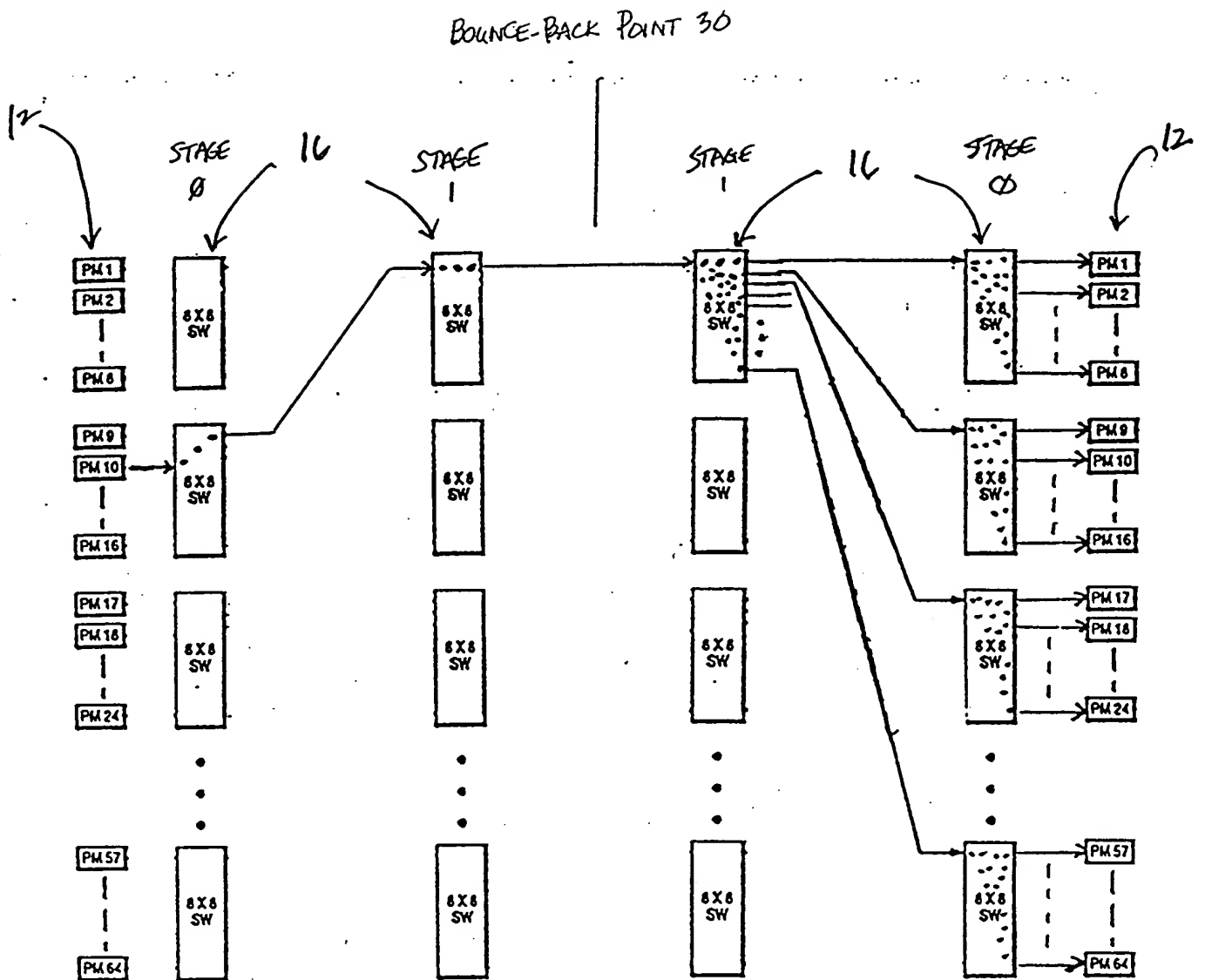


Figure 26

